
Fundamentals Of Semiconductor Processing Technology

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fundamentals of on-resistance in load switches - $r_{on} = r_{silicon} + r_{wire} + r_{lead}$ $p_{d,max} = t_{j,max} t_{ja}^{-2}$ $p_{i rd load on} = \dot{v} v r_{iin out on load} = \dot{v}_{in} v_{out} c_{out} r_{out on gnd} c_{in} + \pm load$ switches overview ti

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